EXAMINATION BRANCH

TIME-TABLE **TIME: 09.30 AM TO 12.30 PM**

M.TECH. I SEMESTER (RA18 REGULATION – 2018 & 2019 BATCHES) SUPPLEMENTARY EXAMINATIONS, DECEMBER-2023/JANUARY- 2024

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING
28-12-2023 THURSDAY	Special Machines	Digital System Design	Digital System Design	Computer Integrated Manufacturing	Advanced Data Structures
02-01-2024 TUESDAY	Analysis of Power Electronic Converters	RTL Simulation and Synthesis with PLDs	RTL Simulation and Synthesis with PLDs	Advanced Computer Aided Design & Manufacturing	Machine Learning
04-01-2024 THURSDAY	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR
08-01-2024 MONDAY	Power Electronic Control of DC Drives	--	Analog Integrated Circuit Design	Advanced Material Technology	Distributed Systems
10-01-2024 WEDNESDAY	--	Embedded C	Embedded C	--	Software Process &Project Management
12-01-2024 FRIDAY	HVDC Transmission	ARM Architectures	--	Special Manufacturing Process	--

NOTE: ANY CLASHES / OMMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.

CONTROLLER OF EXAMINATIONS

Dt.: 20.12.2023

SR Engineering College
Innovation . Creativity . Entrepreneurship

EXAMINATION BRANCH

TIME-TABLE   TIME: 09.30 AM TO 12.30 PM

M.TECH. I SEMESTER (RA15 REGULATION – 2017 BATCH) SUPPLEMENTARY EXAMINATIONS, DECEMBER-2023/JANUARY- 2024

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
28-12-2023 THURSDAY	Machine Modelling & Analysis	**Elective – II** Digital System Design	**Elective – I** Digital System Design	**Elective – II** Precision Engineering	Big data and Analytics	Software Requirements and Estimation
02-01-2024 TUESDAY	Analysis of Power Electronic Converters	**Elective – I** VLSI Technology and Design	VLSI Technology and Design	Advanced Computer Aided Design & Manufacturing	Machine Learning	Web Technologies and Services
04-01-2024 THURSDAY	Modern Control Theory	Real Time Operating System	CMOS Analog Integrated Circuit Design	Theory of Metal Cutting	**Elective – II** Software Architecture and Design Patterns	**Elective – I** Information Security and Audit
08-01-2024 MONDAY	Power Electronic Control of DC Drives	Embedded System Design	Embedded System Design	**Elective – III** Applied Tribology	Distributed Systems	Software Development Methodologies
10-01-2024 WEDNESDAY	**Elective – II** Dynamics of Electrical Machines	**Elective – III** TCP/IP Networks	**Elective – II** CPLD and FPGA Architectures and Applications	Automation in Manufacturing	**Elective – I** Software Process and Project Management	**Elective – II** Cyber Security and Cyber Laws
12-01-2024 FRIDAY	**Elective – I** High Voltage DC Transmission	ARM Architectures	**Elective – III** Advanced Data Communications	**Elective – I** Special Manufacturing Process	Advanced Data Structures and Algorithms	Advanced Data Structures and Algorithms

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CONTROLLER OF EXAMINATIONS

Dt.: 20.12.2023

EXAMINATION BRANCH

TIME-TABLE   **TIME: 09.30 AM TO 12.30 PM**

M.TECH. II SEMESTER (RA18 REGULATION – 2018 & 2019 BATCHES) SUPPLEMENTARY EXAMINATIONS, DECEMBER-2023/JANUARY- 2024

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING
29-12-2023 FRIDAY	Power Electronic Control of AC Drives	Embedded OS & RTOS	CMOS VLSI Design	Manufacturing Systems Simulation	Advanced Algorithms
03-01-2024 WEDNESDAY	Power Electronic Applications to Renewable Energy	--	ASIC and SOC	Theory of Metal Cutting and Forming	Big Data Analytics
05-01-2024 FRIDAY	Microprocessor and Microcontroller	Advanced Digital Signal Processing	Advanced Digital Signal Processing	Advanced Tool Design	Deep Learning
09-01-2024 TUESDAY	--	Embedded Networking / High Speed Digital Design	Embedded Networking / High Speed Digital Design	NDE & NDT	--
11-01-2024 THURSDAY	Power Quality / Dynamics of Electrical Machines	System On Chip Architecture	--	--	Cryptography and Network Security

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CONTROLLER OF EXAMINATIONS

Dt.: 20.12.2023

EXAMINATION BRANCH

TIME-TABLE   **TIME: 09.30 AM TO 12.30 PM**

M.TECH. II SEMESTER (RA15 REGULATION – 2017 BATCH) SUPPLEMENTARY EXAMINATIONS, DECEMBER-2023/JANUARY- 2024

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
29-12-2023 FRIDAY	Power Electronic Control of AC Drives	CPLD and FPGA Architectures and Applications	Real Time Operating Systems	Manufacturing Systems Simulation Modelling & Analysis	Advanced Network Programming	Software Process and Project Management
03-01-2024 WEDNESDAY	Neural And Fuzzy Systems	Hardware Software Co-Design / Network Security and Cryptography	CMOS Mixed Signal Circuit Design	Quality Engineering in Manufacturing	High Performance Computing	Big Data and Analytics
05-01-2024 FRIDAY	Microprocessor and Microcontroller	Digital Signal Processors and Architectures	Embedded C / Digital Signal Processors and Architectures	Materials Technology	Adhoc and Sensor Networks / Database Security	Scripting Languages
09-01-2024 TUESDAY	Flexible AC Transmission Systems (FACTS)	Embedded Networking	Embedded Networking	Production & Operations Management	Web Services and Service Oriented Architecture	Cloud Computing
11-01-2024 THURSDAY	Power Quality	System On Chip Architecture / Sensors and Actuators	Ad-hoc Wireless and Sensor Networks / Sensors and Actuators	Concurrent Engineering & Product Life Cycle Management	Software Quality Assurance & Testing	Software Quality Assurance and Testing
16-01-2024 TUESDAY	Reliability Engineering	Device Modelling / Wireless LANs and PANs	Low Power VLSI Design	Design and Manufacturing of MEMS & Micro Systems	Number theory and Cryptography	Software Architecture and Design Patterns

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CONTROLLER OF EXAMINATIONS

Dt.: 20.12.2023

TIME-TABLE

II YEAR M.TECH. I SEMESTER (RA18 REGULATION – 2018, 2019 BATCHES) SUPPLEMENTARY EXAMINATIONS, DECEMBER-2023/JANUARY- 2024

TIME: 01.30 PM TO 04.30 PM

Date & Day	SUBJECTS (RA18 – 2018, 2019 Admitted Batches)				
	POWER ELECTRONICS	**EMBEDDED SYSTEMS**	**ELECTRONICS DESIGN TECHNOLOGY**	**COMPUTER SCIENCE & ENGINEERING**	**ADVANCED MANUFACTURING SYSTEMS**
28-12-2023 THURSDAY	FLEXIBLE AC TRANSMISSION SYSTEMS (FACTS)	DESIGN FOR TESTABILITY	DESIGN FOR TESTABILITY	NATURAL LANGUAGE PROCESSING	MANUFACTURING POLYMERS
02-01-2024 TUESDAY	OPERATIONS RESEARCH	OPERATIONS RESEARCH	OPERATIONS RESEARCH	CYBER LAWS	INDUSTRIAL SAFETY
	INDUSTRIAL SAFETY	INDUSTRIAL SAFETY	INDUSTRIAL SAFETY		

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CONTROLLER OF EXAMINATIONS

Dt.: 20.12.2023